

AMENDMENT TRANSMITTAL LETTER			Docket No. 02008/071003	
Application No. 10/779,904-Conf. #9608	Filing Date February 17, 2004	Examiner O. A. Louie	Art Unit 2436	

Applicant(s): Masahiro Ishida et al.

Invention: METHOD AND APPARATUS FOR DEFECT ANALYSIS OF SEMICONDUCTOR INTEGRATED CIRCUIT

TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.
The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	9	- 20 =	0	x 52.00	0.00
Independent Claims	3	- 3 =	0	x 220.00	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					

☒ Large Entity
 ☐ Small Entity

☒ No additional fee is required for this amendment.

☐ Please charge Deposit Account No. 50-0591 in the amount of \$ _____.

☐ A check in the amount of \$ _____ to cover the filing fee is enclosed.

☐ Payment by credit card.

☒ The Director is hereby authorized to charge and credit Deposit Account No. 50-0591 as described below.

☒ Credit any overpayment.

☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.

/Thomas K. Scherer/

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Dated: March 10, 2009

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Masahiro Ishida et al.

Application No.: 10/779,904

Confirmation No.: 9608

Filed: February 17, 2004

Art Unit: 2436

For: METHOD AND APPARATUS FOR DEFECT
ANALYSIS OF SEMICONDUCTOR
INTEGRATED CIRCUIT

Examiner: O. A. Louie

REPLY UNDER 37 CFR §1.116

MS: AFTER FINAL
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Madam:

In response to the final Office Action dated January 15, 2009, please reconsider
this application in view of the following.